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Kristin R. Hayes (Depositor's name)  
  
June 8, 2006 (Date)

APPLICATION NO.	FILED DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/689,233	10/20/2003	Kia Song Low	02 P 15173 US / INTECH	4063

TITLE OF INVENTION: INCLUSION OF LOW-K DIELECTRIC MATERIAL BETWEEN BIT LINES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	06/14/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
SARKAR, ASOK K	2891	438-622000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).  
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06/09/2006/Slater & Matsil, LLP 10689233

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Infineon Technologies AG Munich, Germany  
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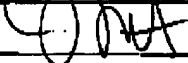
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Date June 8, 2006

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### FACSIMILE COVER SHEET

To: Commissioner for Patents	Total Pages Sent: 3
Mail Stop Issue Fee	(including cover sheet)
Facsimile Number: (571) 273-2885	Transmission Date: June 8, 2006

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Low, <i>et al.</i>	Docket No.: 02 P 15173 US / INTECH 3.
Serial No: 10/689,233	Art Unit: 289I
Date Filed: October 20, 2003	Examiner: Asok K. Sarkar
Title: Inclusion of Low-k Dielectric Material between Bit Lines	

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- Part B – Fee Transmittal (2 duplicate pages)

Respectfully submitted,

Kristin R. Hayes  
 Legal Assistant

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